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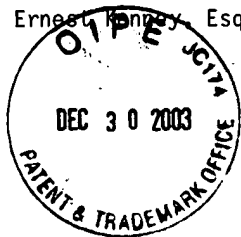
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11/10/2003

BACON & THOMAS, PLLC  
 625 SLATERS LANE  
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 ALEXANDRIA, VA 22314

J. Ernest Kennedy, Esq.



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/926,791	03/22/2002	Thomas Grassl	GRAS3005	8662

TITLE OF INVENTION: SEMICONDUCTOR MEMORY CHIP MODULE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	02/10/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TRAN, MICHAEL THANH	2818	365-063000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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GIESECKE &amp; DEVRIENT GmbH

GERMANY

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(Authorized Signature)

(Date)

Justin J. Cassell, Reg. No. 46,205

12/30/03

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01/07/2004 MAHMED2 00000062 09926791

01 FC:1501  
 02 FC:8001

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF:

THOMAS GRASSL

SERIAL No.: 09/926,791

FILED: March 22, 2002

FOR: SEMICONDUCTOR MEMORY CHIP MODULE

GROUP ART UNIT: 2818

EXAMINER: Michael T. TRAN

ATTY. REFERENCE: GRAS3005/JEK/JJC

Confirmation No.: 8662

Customer No.: 23364

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Sir:

The below identified communication(s) or document(s) is(are) submitted in the above application or proceeding:

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| <input type="checkbox"/>                   | <u>\$1,330 - Patent copies \$12)</u>  |
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☐ Small Entity Status is claimed.

☐

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DATE: December 30, 2003*Respectfully submitted,***JUSTIN J. CASSELL***Attorney for Applicant*

Registration Number: 46,205